

ABSTRACT OF THE DISCLOSURE

An integrator circuit includes an input conductor for conducting an input current, a first amplifier stage having a first input coupled to the input conductor and a second input coupled to receive a reference voltage and a second amplifier stage having a output and an input coupled to an output of the first amplifier stage. An integrating capacitor is coupled between the first input of the first amplifier stage and the output of the second amplifier stage, and a compensation capacitor comprised of an MOS capacitor is coupled between the input and the output of the second amplifier stage. The integrator circuit is especially adapted for use in a CT scanner data acquisition system.

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